

# Semicustom Arrays for the Implementation of Front-End Electronics ICs

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## Abstract

The approach to designing particular functional units and finished channels of front-end electronics on the basis of especially elaborated bipolar semicustom arrays (SA) is grounded. Brief data on the earlier elaborated bipolar non-complementary SA and the ICs, created on its basis, are presented. The structural peculiarities and characteristics of a new bipolar complementary SA and those of the signal processing circuits, placed on the latter, are described. The processing circuits are intended to handle particularly the signals of a solid-state electron multiplier - the analog of PMT.

## References

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